

SCD8660

Adjustable Positive Voltage Regulator

VRG8660

Features

- Manufactured using Linear Technologies Space Qualified RH117 die
- Radiation performance
 - Total dose: ≥ 100 krad(Si), Dose rate = 50-300 rad(Si)/s
 - ELDRS: ≥ 50 krad(Si), Dose rate = 0.01 rad(Si)/s
- Thermal shutdown
- Output voltage adjustable: 1.25V to 37V
- 3-Terminal
- Output current: 1.5A
- Voltage reference: 1.25V $\pm 4\%$
- Load regulation: 1.9% max
- Line regulation: 0.06%/V max
- Ripple rejection: >66 dB
- Packaging – Hermetic Ceramic
 - SMD-0.5 Surface mount
 - 3 Pads, .400"L x .296"W x .120"Ht
 - Power package
 - Weight - 2 gm max
- Designed for aerospace and high reliability space applications
- **Radiation Hardness Assurance Plan: DLA Certified to MIL-PRF-38534, Appendix G.**

Description

The VRG8660 consists of a Positive Adjustable (RH117) voltage regulator capable of supplying 1.5Amps over the output voltage range as defined under recommended operating conditions. The VRG8660 offers excellent line and load regulation specifications and ripple rejection. The VRG8660 serves a wide variety of applications including High Efficiency Linear Regulators, Post Regulators for Switching Supplies, Constant Current Regulators, Battery Chargers and Microprocessor Supply.

The VRG8660 has been specifically designed to meet exposure to radiation environments and is configured for a SMD-0.5 SMT power package. It is guaranteed operational from -55°C to $+125^{\circ}\text{C}$. Available screened to MIL-STD-883, the VRG8660 is ideal for demanding military and space applications.

Dropout ($V_{\text{IN}} - V_{\text{OUT}}$) decreases at lower load currents.

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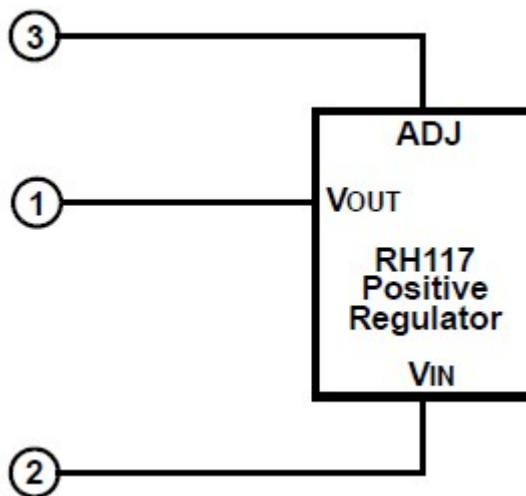


Figure 1 – Block Diagram / Schematic

Absolute Maximum Ratings

Parameter	Range	Units
Lead temperature (soldering 10 Sec) DC	300	°C
Input-Output Voltage Differential	40	V _{DC}
ESD	1.999 <u>1</u> /	KV
Operating Junction Temperature Range	-55 to +150	°C
Storage Temperature Range	-65 to +150	°C

Note:

1) Meets ESD testing per MIL-STD-883, method 3015, Class 1C.

Notice: Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress rating only; functional operation beyond the “Operation Conditions” is not recommended and extended exposure beyond the “Operation Conditions” may affect device reliability.

Recommended Operating Conditions

Parameter	Range	Units
Output Voltage Range	1.25 to 37	V _{DC}
Case Operating Temperature Range	-55 to +125	°C

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Electrical Performance CharacteristicsUnless Otherwise Specified $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$ & $(V_{\text{IN}} - V_{\text{OUT}}) = 5\text{V}$, $I_{\text{OUT}} = 0.5\text{A}$

Parameter	SYM	Conditions ($P \leq P_{\text{MAX}}$)	MIN	MAX	Units
Reference Voltage <u>1/</u> <u>5/</u>	V_{REF}	$3\text{V} \leq (V_{\text{IN}} - V_{\text{OUT}}) \leq V_{\text{DIFF MAX}}$, $10\text{mA} \leq I_{\text{OUT}} \leq I_{\text{MAX}}$	1.20	1.30	V
Line Regulation <u>1/</u> <u>2/</u>	$\frac{\Delta V_{\text{OUT}}}{\Delta V_{\text{IN}}}$	$3\text{V} \leq (V_{\text{IN}} - V_{\text{OUT}}) \leq V_{\text{DIFF MAX}}$, $I_{\text{OUT}} = 10\text{mA}$	-	0.06	%/V
Load Regulation <u>1/</u> <u>2/</u>	$\frac{\Delta V_{\text{OUT}}}{\Delta I_{\text{OUT}}}$	$10\text{mA} \leq I_{\text{OUT}} \leq I_{\text{MAX}}$, $V_{\text{OUT}} \leq 5\text{V}$	-	60	mV
		$10\text{mA} \leq I_{\text{OUT}} \leq I_{\text{MAX}}$, $V_{\text{OUT}} \geq 5\text{V}$	-	1.2	%
Thermal Regulation	-	$I_{\text{OUT}} = 1.5\text{A}$, $(V_{\text{IN}} - V_{\text{OUT}}) = 13.3\text{V}$, 20ms Pulse, 20W, $T_C = +25^{\circ}\text{C}$	-	0.07	%/W
Ripple Rejection Ratio	-	$V_{\text{OUT}} = 10\text{V}$, $f = 120\text{Hz}$, $C_{\text{ADJ}} = 10\mu\text{F}$	66	-	dB
Adjustment Pin Current <u>1/</u>	I_{ADJ}	-	-	100	μA
Adjustment Pin Current Change <u>1/</u>	ΔI_{ADJ}	$10\text{mA} \leq I_{\text{OUT}} \leq I_{\text{MAX}}$	-	5	μA
		$3\text{V} \leq (V_{\text{IN}} - V_{\text{OUT}}) \leq 40\text{V}$,	-	5	μA
Minimum Load Current <u>1/</u> <u>3/</u>	I_{MIN}	$(V_{\text{IN}} - V_{\text{OUT}}) = 40\text{V}$	-	5	mA
Current Limit <u>1/</u> <u>4/</u>	I_{MAX}	$(V_{\text{IN}} - V_{\text{OUT}}) \leq 15\text{V}$	1.50	-	A
		$(V_{\text{IN}} - V_{\text{OUT}}) = 40\text{V}$, $T_C = +25^{\circ}\text{C}$	0.30	-	A
Long Term Stability <u>3/</u>	$\frac{\Delta V_{\text{OUT}}}{\Delta T_{\text{IME}}}$	$T_A = +125^{\circ}\text{C}$	-	1	%
Thermal Resistance (Junction to Case) <u>3/</u>	Θ_{JC}	-	-	3	$^{\circ}\text{C}/\text{W}$

Notes:

- 1) Specification derated to reflect Total Dose exposure to 100 krad(Si) @ $+25^{\circ}\text{C}$
- 2) Regulation is measured at a constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation. Measurements taken at the output lead must be adjusted for lead resistance.
- 3) Not tested. Shall be guaranteed to the specified limits.
- 4) Pulsed at $<10\%$ duty cycle @ 25°C .
- 5) Testing over 12 watts is not performed over $+25^{\circ}\text{C}$.

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**Maximum Power
Dissipation
(Watts)**

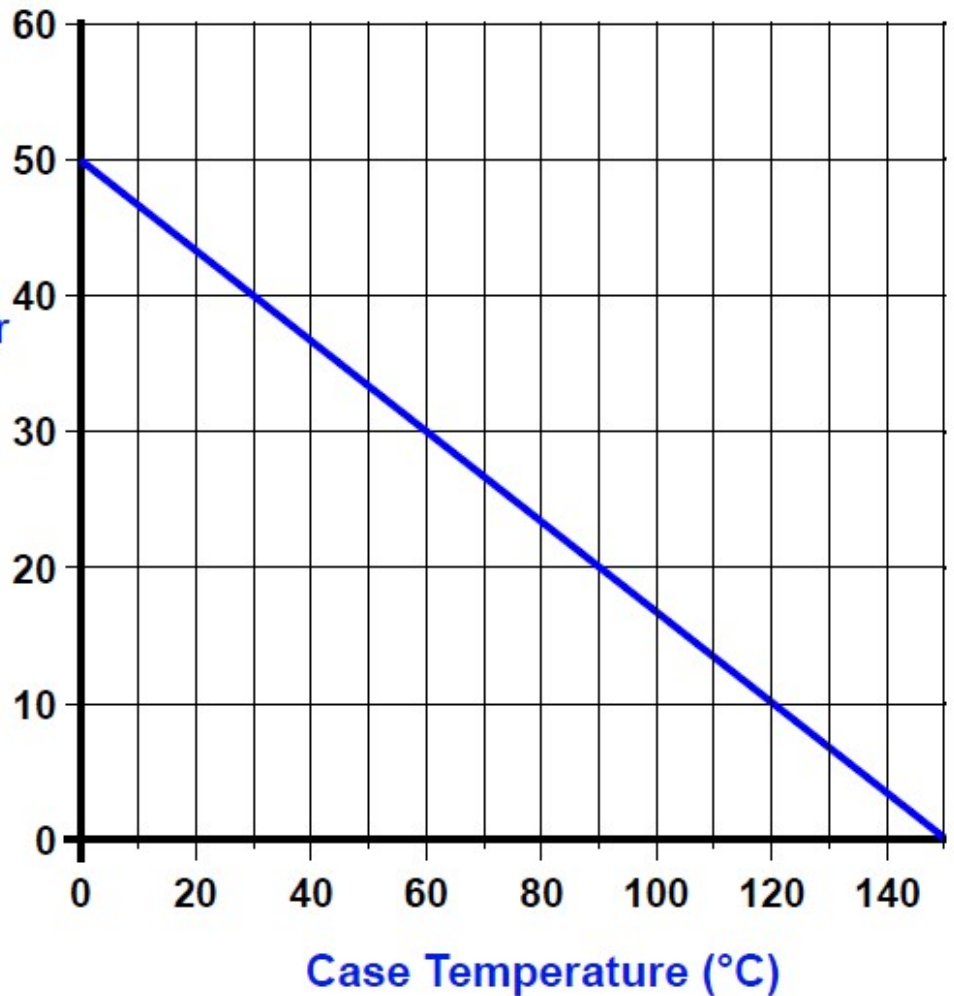


Figure 2 – Maximum Power vs Case Temperature

The maximum Power dissipation is limited by the thermal shutdown function of the regulator chip in the VRG8660. The graph above represents the achievable power before the chip shuts down. The line in the graph represents the maximum power dissipation of the VRG8660 This graph is based on the maximum junction temperature of 150°C and a thermal resistance (θ_{JC}) of 3°C/W.

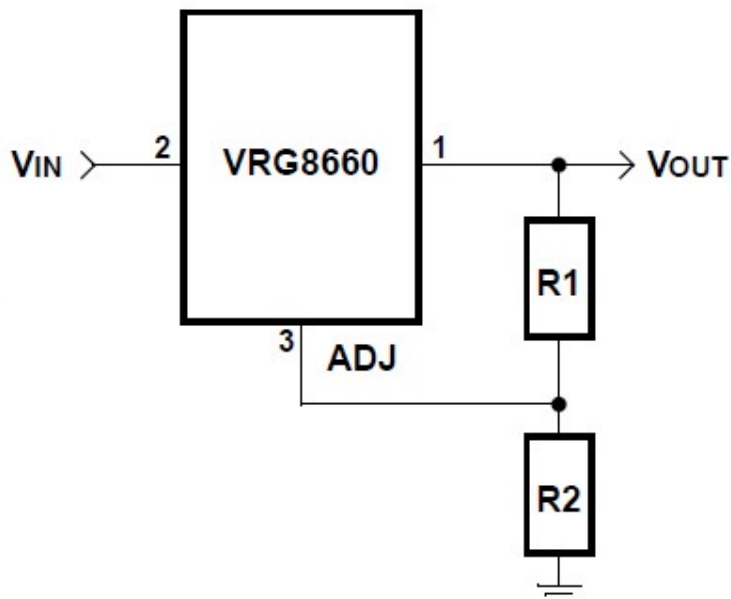
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$V_{REF} = 1.25V, \quad I_{ADJ} = 50\mu A$

$V_{OUT} = V_{REF} (1 + R2/R1) + (I_{ADJ} \times R2)$



Adjustable Regulator
Figure 3 - Typical Applications

Current Limit

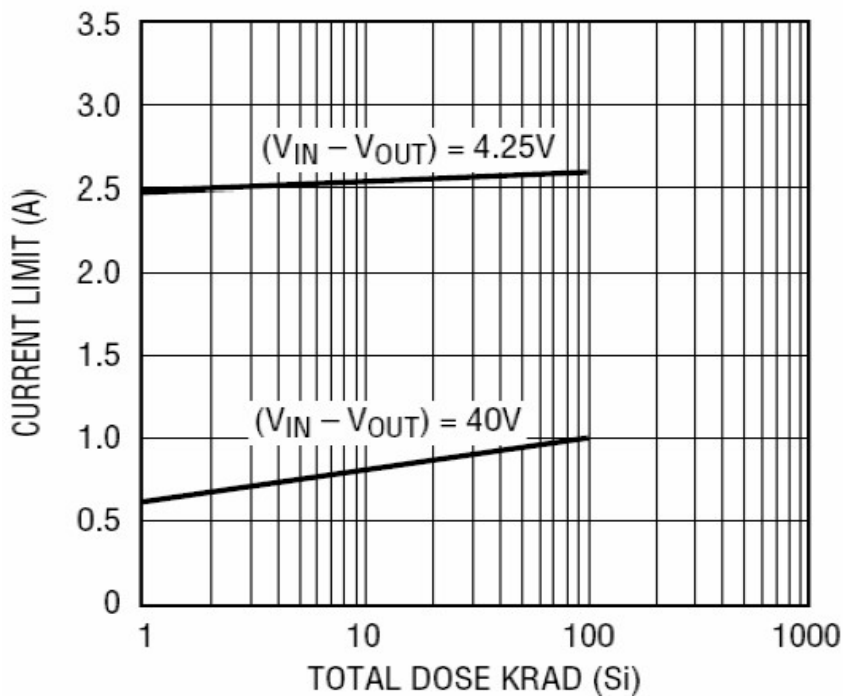


Figure 4 - Typical Current Limit

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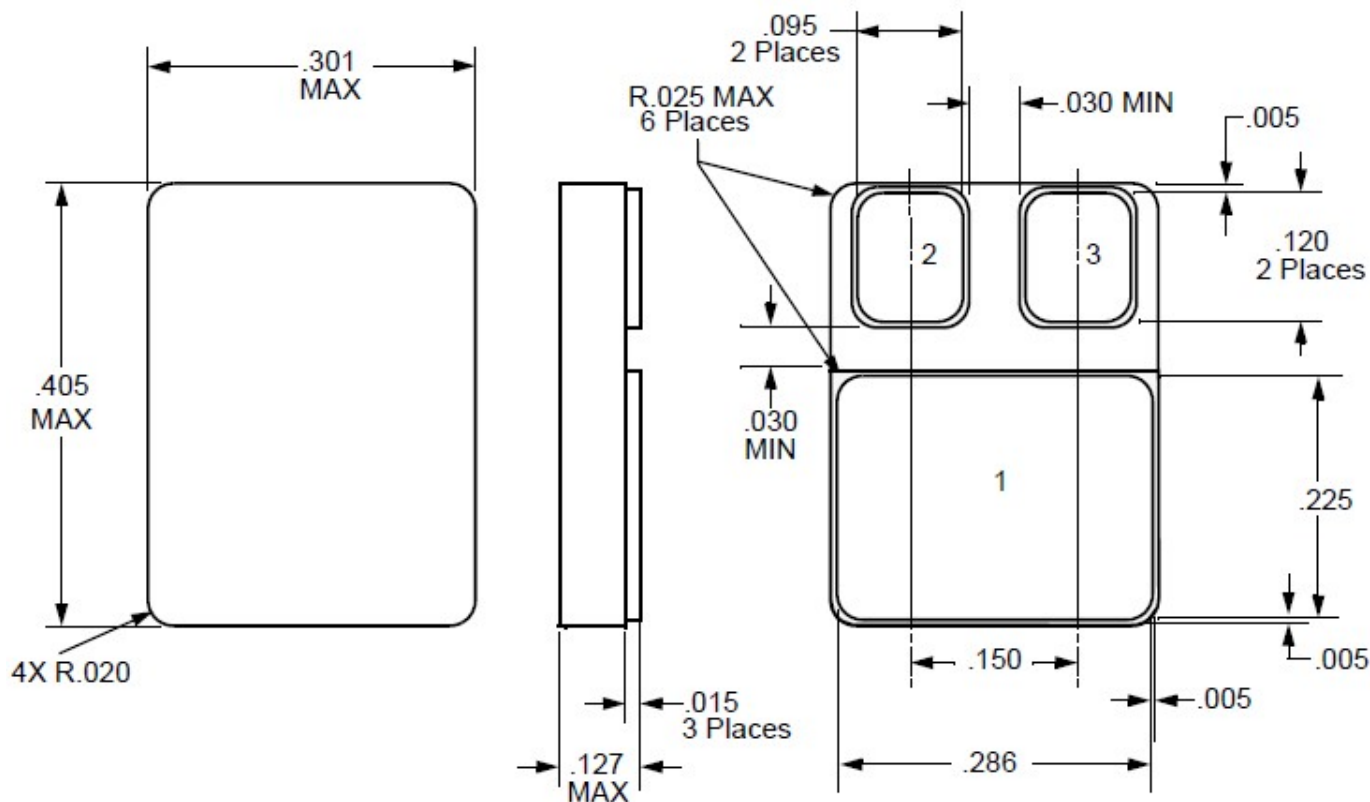


Figure 5 - Package Outline - Surface Mount

Note:

- 1) Package and Lid are electrically isolated from signal pads

Ordering Information

Model	DLA SMD #	Screening	Package
VRG8660 - 7	-	Commercial Flow, +25°C testing only	SMD-0.5 Power Pkg
VRG8660 - S	-	Military Temperature, -55°C to +125°C Screened in accordance with the individual Test Methods of MIL-STD-883 for Space Applications	
VRG8660- 201-1S	5962-0920601KXC	In accordance with DLA SMD	
VRG8660- 201-2S	5962-0920601KXA		
VRG8660- 901-1S	5962R0920601KXC	In accordance with DLA Certified RHA Program Plan to RHA Level "R", 100 krad(Si)	
VRG8660- 901-2S	5962R0920601KXA		

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Revision History

Date	Revision	Change Description
03/24/2016	G	Import into CAES format
03/26/2021	I	Revised per ECN 23566.



Datasheet Definitions

	DEFINITION
Advanced Datasheet	CAES reserves the right to make changes to any products and services described herein at any time without notice. The product is still in the development stage and the datasheet is subject to change . Specifications can be TBD and the part package and pinout are not final .
Preliminary Datasheet	CAES reserves the right to make changes to any products and services described herein at any time without notice. The product is in the characterization stage and prototypes are available.
Datasheet	Product is in production and any changes to the product and services described herein will follow a formal customer notification process for form, fit or function changes.

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